



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

DPW

Applicant: Manakasabapathi Subramanian et al.

Title: Small Scale Wires with Microelectromechanical Devices

Docket No.: 1153.072US1

Serial No.: 10/606,812

Filed: June 26, 2003

Due Date: N/A

Examiner: Unknown

Group Art Unit: 2812

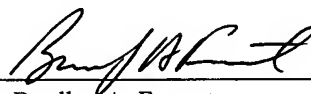
Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):


- ☒ A return postcard.
- ☒ A Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 4 cited documents.
- ☒ The Partial International Search Report from International Application No. PCT/US03/20057 (5 pgs.).

Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
Customer Number 21186

By: 
Atty: Bradley A. Forrest
Reg. No. 30,837

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 28 day of June, 2004.


Name


Signature

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.
(GENERAL)

S/N 10/606,812



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Kanakasabapathi Subramanian et al.	Examiner:	Unknown
Serial No.:	10/606,812	Group Art Unit:	2812
Filed:	June 26, 2003	Docket:	1153.072US1
Title:	Small Scale Wires with Microelectromechanical Devices		

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Supplemental Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

The attached documents were discovered as a result of a Search Report in Applicants' corresponding foreign patent application. Enclosed for the Examiner's information is a copy of the cited documents and the Search Report.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Supplemental Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Supplemental Information Disclosure Statement considered.

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

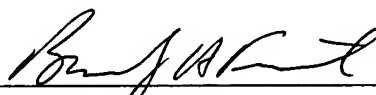
Respectfully submitted,

KANAKASABAPATHI SUBRAMANIAN ET AL.

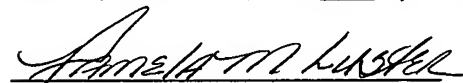
By their Representatives,


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Minneapolis, MN 55402
(612) 373-6972

Date 6-28-2004

By 
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Name


Signature

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use as many sheets as necessary)	Complete if Known	
	Application Number	10/606,812
	Filing Date	June 26, 2003
	First Named Inventor	Subramanian, Kanakasabapathi
	Group Art Unit	2812
	Examiner Name	Unknown
Sheet 1 of 1	Attorney Docket No: 1153.072US1	

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		BOURLAND, S. , et al., "Silicon-on-insulator Processes for the Fabrication of Novel Nanostructures", <u>J. Vac. Sci. Technol. B</u> , 19, No. 5, (Sept. 2001),1995-1997	
		DOHERTY, L. , et al., "Application of MEMS technologies to nanodevices", <u>Proceedings of the 2003 IEEE International Symposium on Circuits and Systems</u> , Vol. 3, (2003),934-937	
		MILANOVIC, V. , et al., "Micromachining Technology for Lateral Filed Emission Devices", <u>IEEE Transactions on Electron Devices</u> , Vol. 48(1), (Jan. 2001),166-173	
		NAMATSU, H. , et al., "Fabrication of One-Dimensional Nanowire Structures Utilizing Crystallographic Orientation in silicon and their Conductance Characteristics", <u>J. Vac. Sci. Technol. B</u> , 15, No. 5, (Sept. 1997),1688-1696	

EXAMINER

DATE CONSIDERED